

## REMARKS

In the Official Action mailed on **10 August 2006**, the Examiner reviewed claim 1. Claim 1 was rejected under 35 U.S.C. §102(e) as being anticipated by Gharachorloo et al (USPub 2002/0083274, hereinafter “Gharachorloo”).

### Rejections under 35 U.S.C. §102(e)

Claim 1 was rejected as being anticipated by Gharachorloo. The Examiner avers that the limitation of a second-level translation lookaside buffer that is coupled to the plurality of first-level translation lookaside buffers is met by Gharachorloo by “data transported through the L2 cache as per paragraph 0145.”

Applicant respectfully points out that the second-level translation lookaside buffer of the present invention stores a subset of the **page table entries** containing virtual-to-physical address translations in the plurality of first-level translation lookaside buffers (see paragraph [0064] of the instant application). This is not the same as storing data in an L2 cache as is done in Gharachorloo. The page table entries stored in the second-level translation lookaside buffer are shared among all of the first-level translation lookaside buffers. There is nothing within Gharachorloo, either explicit or implicit, which suggests storing a subset of the **page table entries** in the plurality of first-level translation lookaside buffers in a second-level translation lookaside buffer.

Accordingly, Applicant has amended independent claim 1 to clarify that the present invention stores a subset of the **page table entries** in the plurality of first-level translation lookaside buffers in a second-level translation lookaside buffer. This amendment finds support in paragraph [0064] of the instant application.

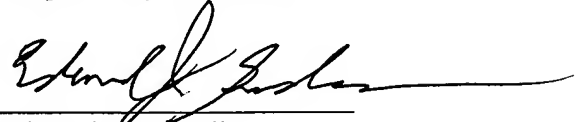
Hence, Applicant respectfully submits that independent claim 1 as presently amended is in condition for allowance.

**CONCLUSION**

It is submitted that the present application is presently in form for allowance. Such action is respectfully requested.

Respectfully submitted,

By



Edward J. Grundler  
Registration No. 47,615

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Edward J. Grundler  
PARK, VAUGHAN & FLEMING LLP  
2820 Fifth Street  
Davis, CA 95618-7759  
Tel: (530) 759-1663  
Fax: (530) 759-1665  
Email: edward@parklegal.com